



FIG. 2

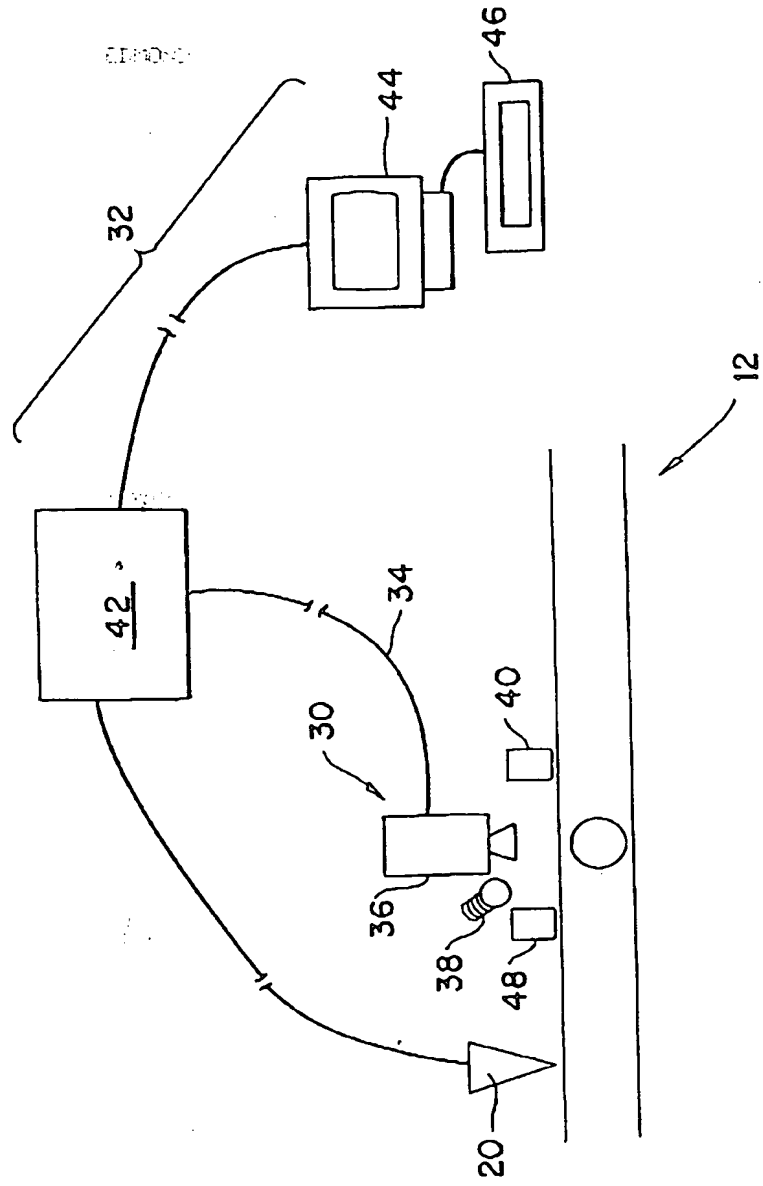


FIG. 3 is a block diagram of a system 50. The system 50 includes four processing units 10, each of which is connected to a common bus 52. The bus 52 is a central communication channel that allows the processing units 10 to exchange data and control signals. The units 10 are arranged in a row, and the bus 52 runs horizontally between them, with vertical lines indicating the connections to each unit.

50

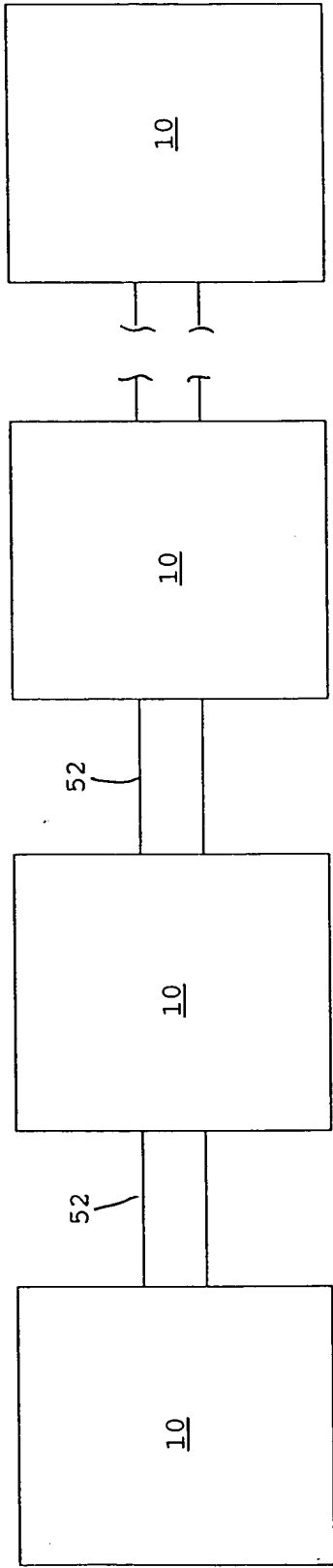


FIG. 3

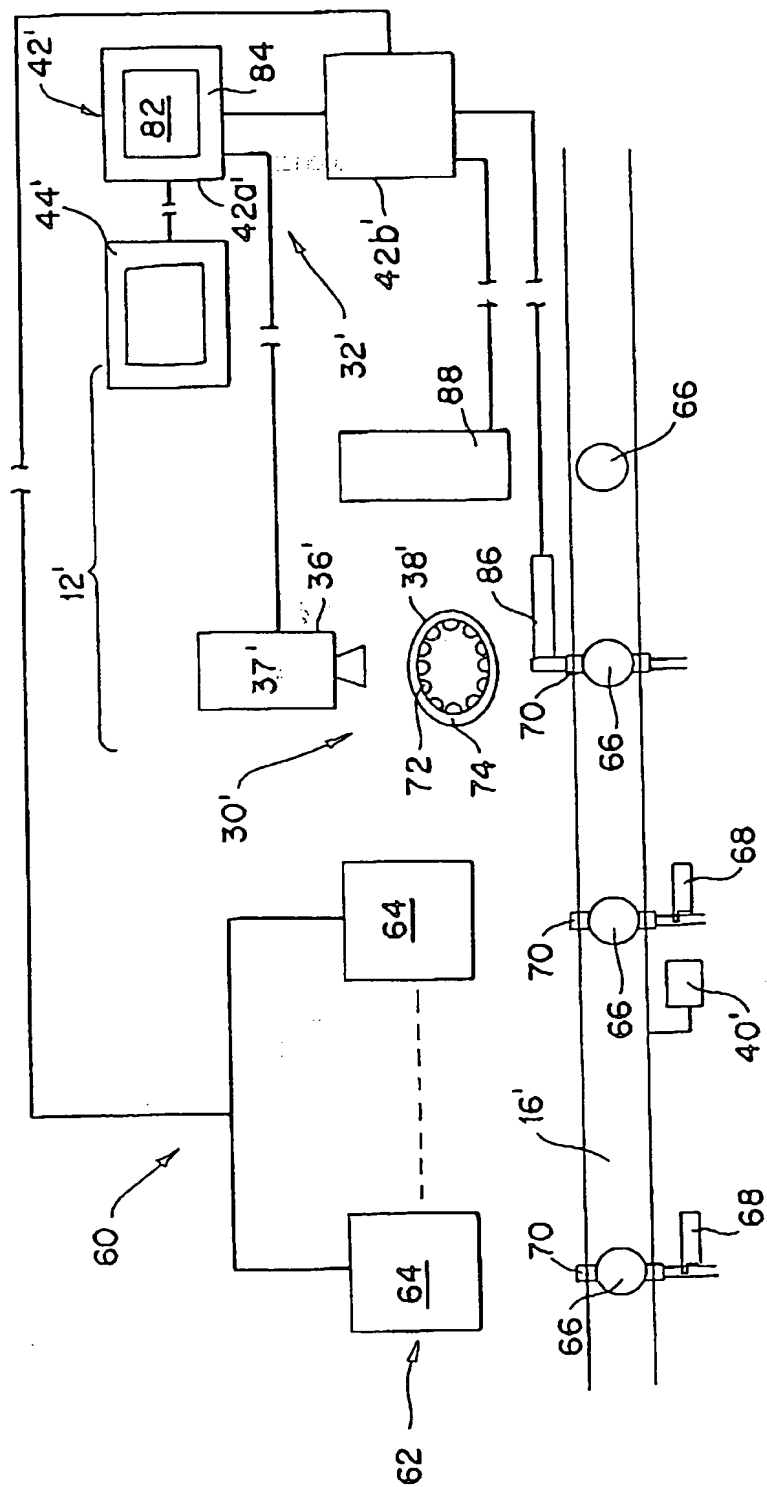


FIG. 4

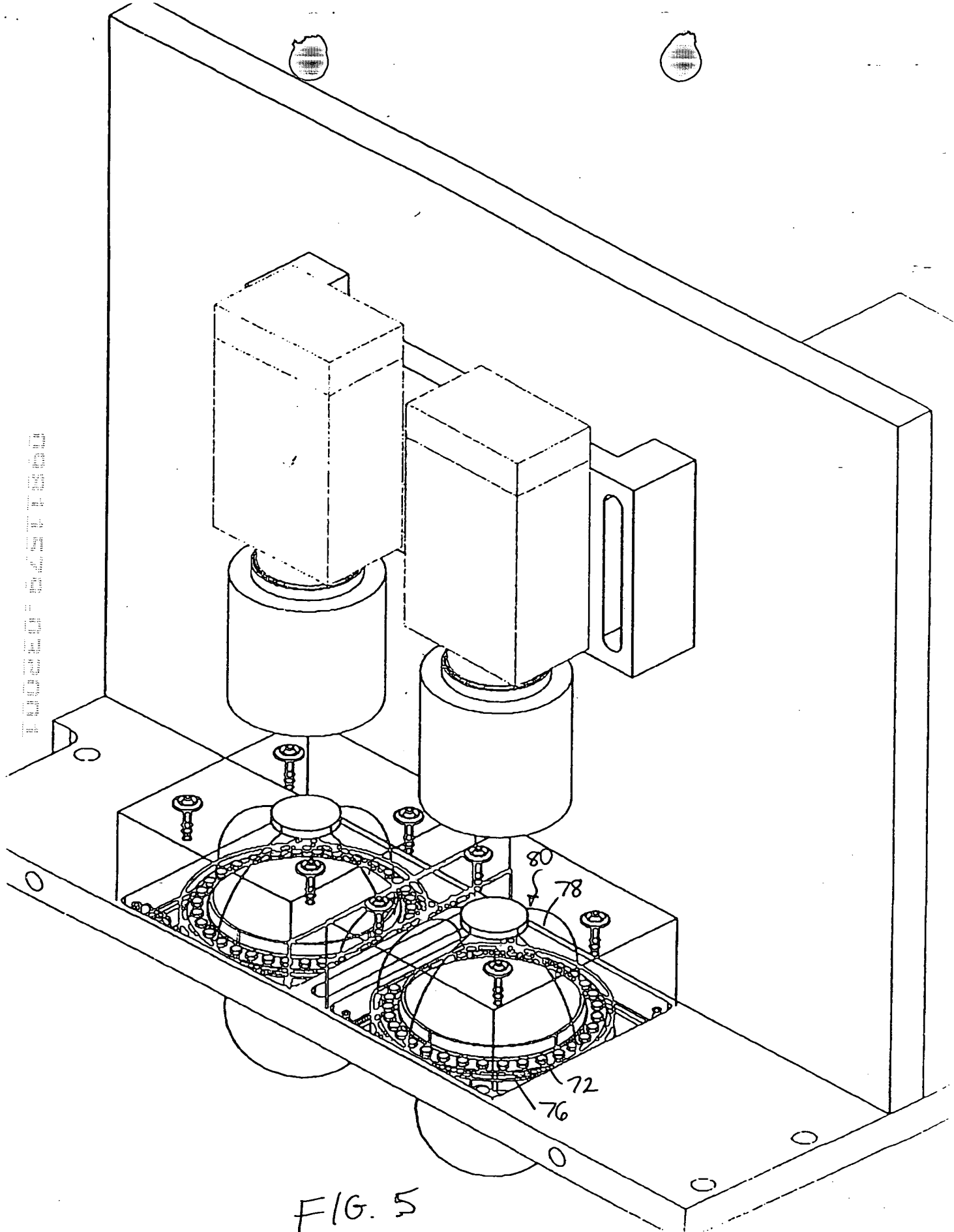
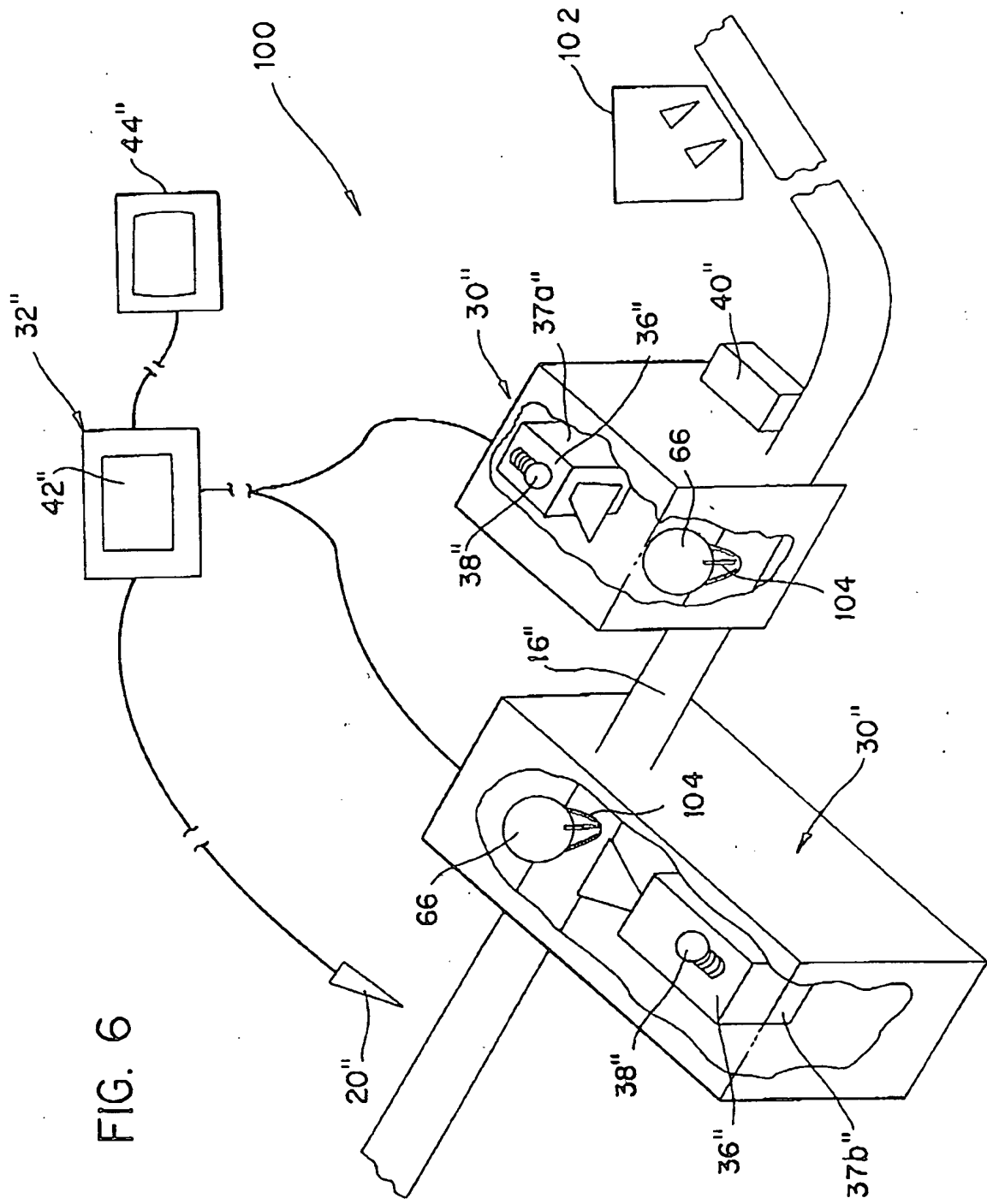


FIG. 5

FIG. 6



1. The system of claim 1, wherein the first processor is configured to receive the first data from the first sensor and the second data from the second sensor, and the first processor is configured to process the first data and the second data to generate the first output.

120

120

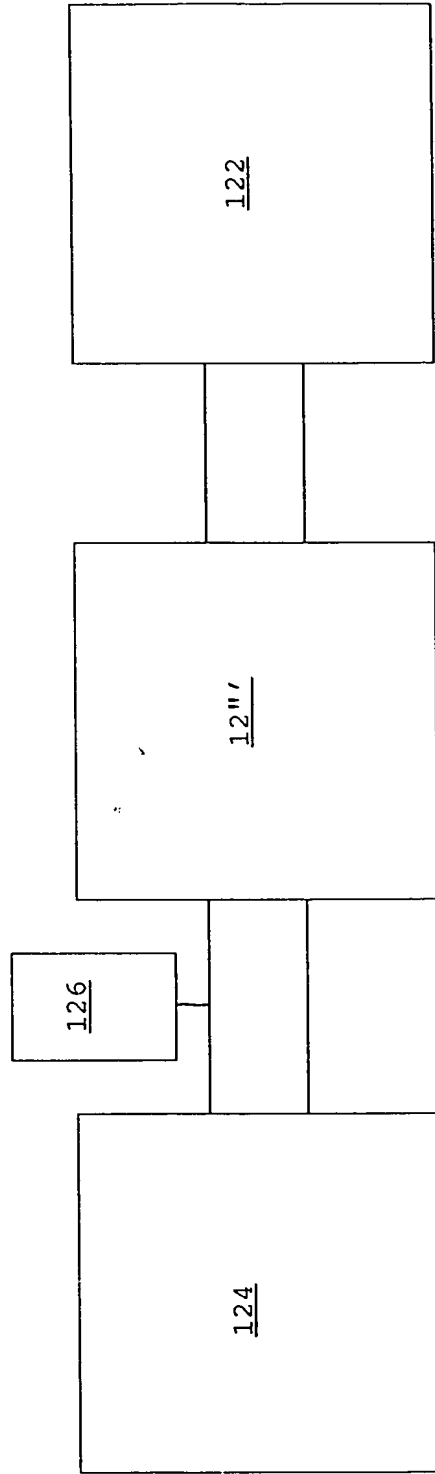


FIG. 7